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1 **A class of soft decoding algorithms**
Ponnampalam, V.; Grant, A.; Vucetic, B.
Information Theory, 2001. Proceedings. 2001 IEEE International Symposium on , 2001
Page(s): 258

[Abstract] [PDF Full-Text (96 KB)] **CNF**

2 **Partitioning of spreading sequences for increased minimum distance using CPFSK modulation**
Fulghum, T.L.; Miller, S.L.
Communications, IEEE Transactions on , Volume: 47 Issue: 4 , April 1999
Page(s): 493 -497

[Abstract] [PDF Full-Text (84 KB)] **JNL**

3 **New results on optimal error-correcting codes**
Ostergard, P.R.J.
Information Theory and Communications Workshop, 1999. Proceedings of the 1999 IEEE , 1999
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4 **Efficient heuristic search algorithms for soft-decision decoding of linear block codes**
Ching-Cheng Shih; Wulff, C.R.; Hartmann, C.R.P.; Mohan, C.K.
Information Theory, IEEE Transactions on , Volume: 44 Issue: 7 , Nov. 1998
Page(s): 3023 -3038

[Abstract] [PDF Full-Text (804 KB)] **JNL**

6 **A methodology for minimum area cellular automata generation**
Cardoso, P.S.; Strum, M.; de A. Amazonas, J.R.; Wang Jiang Chau
Test Symposium, 1998. ATS '98. Proceedings. Seventh Asian , 1998
Page(s): 33 -37

[Abstract] [PDF Full-Text (1152 KB)] **CNF**

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7 **Some best rate 1/p and rate (p-1)/p systematic quasi-cyclic codes over GF (3) and GF(4)**
Gulliver, T.A.; Bhargava, V.K.
Information Theory, IEEE Transactions on , Volume: 38 Issue: 4 , July 1992
Page(s): 1369 -1374

[Abstract] [PDF Full-Text (484 KB)] **JNL**

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8 **Some best rate 1/p and rate (p-1)/p systematic quasi-cyclic codes**
Gulliver, T.A.; Bhargava, V.K.
Information Theory, IEEE Transactions on , Volume: 37 Issue: 3 Part: 2 , May 1991
Page(s): 552 -555

[Abstract] [PDF Full-Text (308 KB)] **JNL**

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9 **Some new constant weight codes**
Koschnick, K.-U.
Information Theory, IEEE Transactions on , Volume: 37 Issue: 2 , March 1991
Page(s): 370 -371

[Abstract] [PDF Full-Text (164 KB)] **JNL**

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10 **Decoding of severely filtered modulation codes using the (M, L) algorithm**
Seshadri, N.; Anderson, J.B.
Selected Areas in Communications, IEEE Journal on , Volume: 7 Issue: 6 , Aug. 1989
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[Abstract] [PDF Full-Text (740 KB)] **JNL**

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1 Pedagogical design: The emporium approach to computer science education 77%

J. A. N. Lee

Proceedings of the 7th annual conference on Innovation and technology in computer science education June 2002

As an alternative to laboratories for computer science programming courses, the Emporium style of learning environment has advantages for a course concerned with the foundations and principles of computer science. Following the lead from courses in freshman mathematics, this paper describes the active learning and assessment methodologies incorporated into a freshman computer science course. The results have been encouraging, and student acceptance of an alternative approach to learning is impro ...

2 A Statistical Model for Relevance Feedback in Information Retrieval 77%

C. T. Yu , W. S. Luk , T. Y. Cheung

Journal of the ACM (JACM) April 1976

Volume 23 Issue 2

A statistical model is presented for the investigation of a practical method used in relevance feedback. A necessary and sufficient condition for the two parameters used in this method to define a better query than the original query is given. A region in the plane of the parameters is shown to satisfy the sufficient condition. While the points for producing optimal queries are not exactly located, they are shown to be lying on a finite portion of a hyperbola. Experimental results support s ...

3 HAPPI: a chip compiler based on double-level-metal technology 77%

Rathin Putatunda , David Smith , Stephen McNeary , James Crabbe

Proceedings of the 23rd ACM/IEEE conference on Design automation July 1986

This paper describes a unique fully automatic chip compiler, HAPPI, that uses double-level-metal technology and 3 levels of interconnection to realize high-speed and maximum-density chip designs consisting of a varying mixture of custom and standard-cell macros within a chip topology that guarantees 100% signal and power routing. A heuristic technique for generating placements of "soft macros" that are balanced in both area and connectivity has been presented. A routing approach ...

4 Interactive simulation of fire in virtual building environments 77%

Richard Bukowski , Carlo Séquin

Proceedings of the 24th annual conference on Computer graphics and interactive techniques August 1997

5 A qualitative simulation approach for fuzzy dynamical models 77%

Andrea Bonarini , Gianluca Bontempi

ACM Transactions on Modeling and Computer Simulation (TOMACS) October 1994

Volume 4 Issue 4

This article deals with simulation of approximate models of dynamic systems. We propose an approach that is appropriate when the uncertainty intrinsic in some models cannot be reduced by traditional identification techniques, due to the impossibility of gathering experimental data about the system itself. The article presents a methodology for qualitative modeling and simulation of approximately known systems. The proposed solution is based on the Fuzzy Sets theory, extending the power of t ...

6 Precision requirements for digital color reproduction 77%

Mike Stokes , Mark D. Fairchild , Roy S. Berns

ACM Transactions on Graphics (TOG) October 1992

Volume 11 Issue 4

An environment was established to perform device-independent color reproduction of full-color pictorial images. In order to determine the required precision for this environment, an experiment was performed to psychophysically measure colorimetric tolerances for six images using paired comparison techniques. These images were manipulated using 10 linear and nonlinear functions in the CIELAB dimensions of lightness, chroma, and hue angle. Perceptibility tolerances were determined using probi ...

Results 1 - 6 of 6 **short listing**

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Results 1 - 6 of 6 **short listing**

1 A Survey of Interactive Graphical Systems for Mathematics 80%
Lyle B. Smith
ACM Computing Surveys (CSUR) December 1970
Volume 2 Issue 4

2 Design of the real-time executive for the Univac(r) 418 system 77%
John Michael Williams
Proceedings of the 21st national conference January 1966
The UNIVAC 418 system hardware The 418 is a small- to medium-scale real-time computer announced to the general public in September of 1964. It is available in two models, identical except for storage speed (two or four microseconds). Storage and registers

3 Feedback vertex sets and cyclically reducible graphs 77%
Ching-Chy Wang , Errol L. Lloyd , Mary Lou Soffa
Journal of the ACM (JACM) April 1985
Volume 32 Issue 2
The problem of finding a minimum cardinality feedback vertex set of a directed graph is considered. Of the classic NP-complete problems, this is one of the least understood. Although Karp showed the general problem to be NP-complete, a linear algorithm for its solution on reducible flow graphs was given by Shamir. The class of reducible flow graphs is the only nontrivial class of graphs for which a polynomial-time algorithm to solve this problem is known. The main result of this paper is to ...

4 Modeling ASIC memories in VHDL 77%
E. Balaji , P. Krishnamurthy
Proceedings of the conference with EURO-VHDL'96 and exhibition on European Design Automation September 1996

5 An exact algorithm for selecting partial scan flip-flops 77%
Srimat T. Chakradhar , Arun Balakrishnan , Vishwani D. Agrawal
Proceedings of the 31st annual conference on Design automation conference June 1994

6 Implicit computation of minimum-cost feedback-vertex sets for partial scan and other applications 77%

Pranav Ashar , Sharad Malik

Proceedings of the 31st annual conference on Design automation conference June 1994

Results 1 - 6 of 6 **short listing**

	Type	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition
1	IS&R	2	("5187675").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/09 18:10		
2	BRS	16	("3740538" | "4567572" | "4644560" | "4783780" | "4850033" | "4901307" | "4903005" | "4930140" | "4965850" | "4984247" | "5022049" | "5038399" | "5056109" | "5101501" | "5103459" | "5109390").PN.	USPAT	2002/10/03 18:09		
3	BRS	16	5187675.URPN.	USPAT	2002/10/03 18:10		
4	BRS	6	("search circuit" with minimum) same maximum	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/03 18:13		
5	BRS	33	"search circuit" with minimum	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/07 11:16		
6	BRS	986	"search circuit"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/07 11:29		
7	BRS	18	"search circuit" with binary	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/07 11:29		
8	BRS	1044	"binary decision"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/07 15:41		
9	BRS	209	"binary operator"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/07 15:43		
10	BRS	715	"partial address"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/07 15:43		
11	BRS	0	"binary operator" same "partial address"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/07 15:43		
12	BRS	1	"binary decision" same "partial address"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/07 15:43		
13	BRS	3	"binary decision" and "partial address"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/07 17:08		
14	BRS	0	"sesarch tree"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/07 15:46		
15	BRS	947	"search tree"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/07 15:47		
16	BRS	181	binary adj1 "search tree"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/07 15:47		
17	BRS	3	(binary adj1 "search tree") and "partial address"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/07 15:47		
18	BRS	197	"sort processor"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/07 17:08		
19	BRS	103	"sort processor" and JP	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/07 17:09		
20	BRS	0	"sort processor" and "02309365"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/07 17:09		
21	BRS	1	"sort processor" and "04180124"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/08 08:50		
22	BRS	112289	data near2 address	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/08 08:50		
23	BRS	179	(data near2 address) near2 partial	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/08 08:51		
24	BRS	8192	"decision unit"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/08 08:51		

	Type	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition
25	BRS	2	((data near2 address) near2 partial) and "decision unit"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/08 08:52		
26	BRS	209	"binary operator"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/08 08:53		
27	BRS	0	((data near2 address) near2 partial) and "binary operator"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/08 08:53		
28	BRS	0	((data near2 address) near2 partial) with tree	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/08 08:53		
29	BRS	0	((data near2 address) near2 partial) same tree	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/08 08:53		
30	BRS	6	((data near2 address) near2 partial) and tree	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/08 08:57		
31	BRS	257	"computation stage"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/08 08:57		
32	BRS	1	((data near2 address) near2 partial) and "computation stage"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/08 08:58		
33	BRS	1	"decision unit" and "computation stage"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/08 08:58		
34	BRS	38	(data near2 address) and "computation stage"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/08 09:22		
35	BRS	399	optimiz$8 near3 tree	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/08 09:22		
36	BRS	29	(optimiz$8 near3 tree) with search	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/08 09:23		
37	BRS	19	((optimiz$8 near3 tree) with search) and binary	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/08 09:33		
38	BRS	3	(((optimiz$8 near3 tree) with search) and binary) and partial	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/08 09:34		
39	BRS	716	"partial address"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/08 09:35		
40	BRS	12	("decision unit" or "binary operator" or "computation stage" or (optimiz$8 near3 tree)) and "partial address"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/08 09:50		
41	IS&R	2	("5187675").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/08 11:01		
42	IS&R	1096	(340/146.2).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/08 14:24		
43	BRS	11284	address near2 result	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/08 14:25		
44	BRS	257	"computation stage"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/08 14:25		
45	BRS	11	"computation stage" and (address near2 result)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/08 14:33		
46	BRS	798	"binary operation"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/08 14:33		
47	BRS	17	"binary operation" with address	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/08 14:33		
48	IS&R	2	("5710562").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/11 09:55		
49	IS&R	4	("4100532").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/09 17:53		

	Type	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition
50	BRS	168390	compar$6 near5 data	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/09 17:54		
51	BRS	50377	value near5 address	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/09 17:54		
52	BRS	902	(compar$6 near5 data) with (value near5 address)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/09 17:54		
53	BRS	3	((compar$6 near5 data) with (value near5 address)) with partial	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/09 17:57		
54	BRS	8020	(data near2 address) with compar$6	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/09 17:58		
55	BRS	39	((data near2 address) with compar$6) with partial	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/09 17:58		
56	BRS	16	("3740538" | "4567572" | "4644560" | "4783780" | "4850033" | "4901307" | "4903005" | "4930140" | "4965850" | "4984247" | "5022049" | "5038399" | "5056109" | "5101501" | "5103459" | "5109390").PN.	USPAT	2002/10/09 18:09		
57	BRS	16	5187675.URPN.	USPAT	2002/10/09 18:09		
58	BRS	9	((("3740538" | "4567572" | "4644560" | "4783780" | "4850033" | "4901307" | "4903005" | "4930140" | "4965850" | "4984247" | "5022049" | "5038399" | "5056109" | "5101501" | "5103459" | "5109390").PN.) or 5187675.URPN.) and partial	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/09 18:13		
59	BRS	35280	"specific value"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/09 18:13		
60	BRS	164	"specific value" near5 locat$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/09 18:14		
61	BRS	40	("specific value" near5 locat$5) and partial	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/09 18:14		
62	BRS	716	"partial address"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/09 18:14		
63	BRS	5	("specific value" near5 locat$5) and "partial address"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/10 11:08		
64	BRS	16303	"neural network"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/10 11:08		
65	BRS	253	"neural network" with search	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/10 11:09		
66	BRS	20	("neural network" with search) with fuzzy	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/10 11:09		
67	BRS	2	("neural network" with search) and "partial address"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/10 11:19		
68	BRS	717	"partial address"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/10 11:19		
69	BRS	4	"neural network" and "partial address"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/10 11:23		
70	BRS	17	("neural network" with search) with compar$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/10 11:26		

	Type	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition
71	BRS	110	"partial address" with compar$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/10 11:27		
72	BRS	0	("partial address" with compar$5) with search	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/10 11:27		
73	BRS	3	("partial address" with compar$5) same search	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/11 09:06		
74	IS&R	2	("5710562").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/11 09:11		
75	IS&R	2	("4821290").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/11 09:13		
76	IS&R	2	("5710562").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/11 09:13		
77	IS&R	2	("5187675").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/11 09:55		